

Software Performance Engineering

Recitation 1.2

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Tuesday, Sep 9, 2025



COURSE LOGISTICS



Homework 2 is released!

- You have a checkoff part and the actual homework part
- Checkoff is due: tonight 10 pm
- Homework 2 is due: Monday September 15

- Project 1 beta: September 23, Tuesday
- Project 1 final: October 2, Thursday

- Reach tier 25 for Project 1 beta to get **at least B grade!**

Gradescope

- For write-ups, when you make your submission, please link each question to the page that you answer is on.
- Eg: for question 1, your answer is on page 1 and 2. For question 2, your answer is on page 2 and 3.
- It is ok to link multiple pages to one question!
- This makes it easier for TA to clearly see your submission for every question and don't miss anything!

Gradescope

- Make sure you follow the handout **carefully** and submit **all the required materials**.
- Write-up 12: Compile again with `make clean`; make `LOCAL=1 UBSAN=1`. What do you see when you run `./is_power_of_two`? Paste the error that the sanitizer throws. In addition, explain where and why this error happened. Fix the error you found and explain your fix. Finally, compile and run the program again and verify that the output is correct. Paste your program output.

CODING WARM UP & QA TIME



PROJECT 1: CONTINUE



```

29 void rotate_bit_matrix(uint8_t *img, const bits_t N) {
30     // Get the number of bytes per row in `img`
31     const uint32_t row_size = bits_to_bytes(N);
32
33     uint32_t w, h, quadrant;
34     for (h = 0; h < N / 2; h++) {
35         for (w = 0; w < N / 2; w++) {
36             uint32_t i = w, j = h;
37             uint8_t tmp_bit = get_bit(img, row_size, i, j);
38
39             // Move a bit from one quadrant to the next and do this
40             // for all 4 quadrants of the `img`
41             for (quadrant = 0; quadrant < 4; quadrant++) {
42                 uint32_t next_i = N - j - 1, next_j = i;
43                 uint8_t save_bit = tmp_bit;
44
45                 tmp_bit = get_bit(img, row_size, next_i, next_j);
46                 set_bit(img, row_size, next_i, next_j, save_bit);
47
48                 // Update the `i` and `j` with the next quadrant's values and
49                 // the `next_i` and `next_j` will get the new destination values
50                 i = next_i;
51                 j = next_j;
52             }
53         }
54     }
55
56     return;
57 }

```

First step: block-wise rotation

A	B	C	D
E	F	G	H
I	J	K	L
M	N	O	P

(a) Original image

M	B	C	A
E	F	G	H
I	J	K	L
P	N	O	D

(b) Image after translating blocks

Next step: in-block rotation

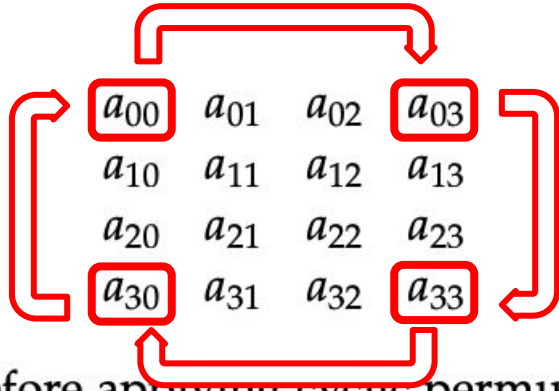
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(b) Image after translating blocks

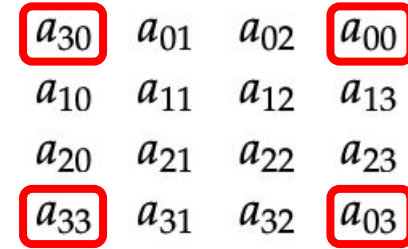
⌊	B	C	⌋
E	F	G	H
I	J	K	L
⌈	N	O	⌋

(c) Image after rotating blocks

Next step: in-block rotation



(a) before applying cyclic permutation



(b) after applying cyclic permutation

One possible approach: row-column-row

- An algorithm for rotating block of bits
- Basic idea:
 - rotate row r left by $r + 1$
 - rotate column c down by $c + 1$
 - rotate row r left by r

Row-column-row algorithm

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Rotate by 4 = no change

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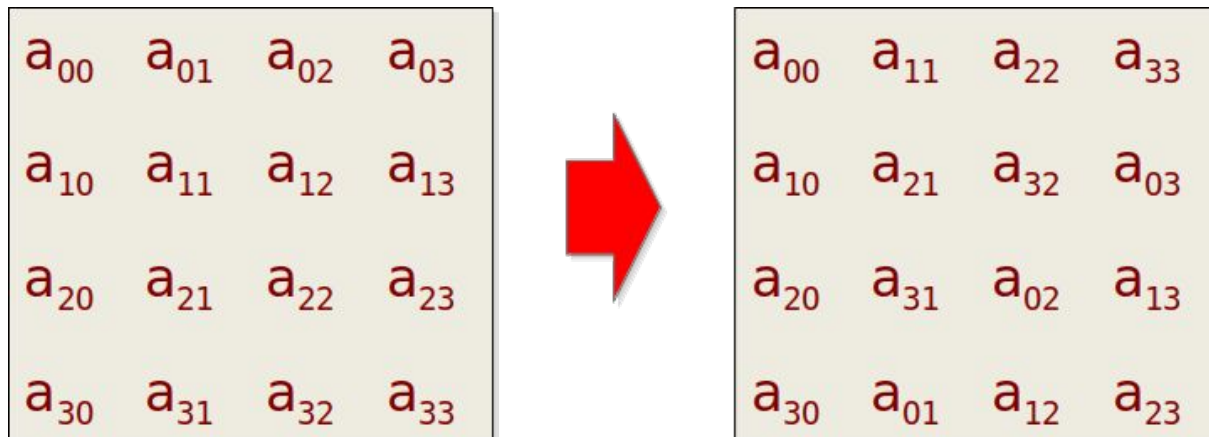
How to implement row rotation?

Operator	Description
&	AND
	OR
^	XOR (exclusive OR)
~	NOT (one's complement)
<<	shift left
>>	shift right

How to implement column rotation?

Input: $N \times N$ matrix of bits, stored in row-major order.

Goal: Circularly rotate i th column of bits up i rows.



In the example that follows, we have $N = 32$. Each row is stored in a 32-bit word, with column 0 in the most-significant bit.

Naive approach

```
const uint32_t N = 32;
const uint32_t mask = 1 << (N-1);
uint32_t A[N];
for (int i = 0; i < N; i++){
    uint32_t col = 0;
    // gather bits in column i
    for (int j = 0; j < N; j++)
        col = col | (((A[j] << i) & mask) >> j);
    // rotate bits in column i
    col = (col << i) | (col >> (N - i));
    // put column i back
    for (int j = 0; j < N; j++)
        A[j] = (A[j] & ~(mask >> i)) |
            (((col << j) >> i) & (mask >> i));
}
```

CODING TIME!



More hints

- Loop unrolling
- Function inlining
- Algebraic identities
- Tiling
- Prefetching
-